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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/496,374	02/02/2000	Masami Kidono	OOCL-11 (11P024627)	6123

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EXAMINER

PIZIALI, JEFFREY J

ART UNIT PAPER NUMBER

2673

DATE MAILED: 03/07/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

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# Office Action Summary

Application No.

09/496,374

Applicant(s)

KIDONO ET AL.

Examiner

Jeff Piziali

Art Unit

2673

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 15 October 2002.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-9 and 11 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-9 and 11 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☒ The proposed drawing correction filed on 15 April 2002 is: a) ☒ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

## Priority under 35 U.S.C. §§ 119 and 120

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

## Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other:



## DETAILED ACTION

### *Continued Examination Under 37 CFR 1.114*

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on October 15, 2002 has been entered.

### *Drawings*

2. This application has been filed with informal drawings which are acceptable for examination purposes only. Formal drawings will be required when the application is allowed.
3. The proposed drawing correction and/or the proposed substitute sheets of drawings, filed on April 15, 2002 have been approved. A proper drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The correction to the drawings will not be held in abeyance.
4. The Patent and Trademark Office no longer makes drawing changes. See 1017 O.G. 4. It is applicant's responsibility to ensure that the drawings are corrected. Corrections must be made in accordance with the instructions below.



## INFORMATION ON HOW TO EFFECT DRAWING CHANGES

### 5. **Correction of Informalities -- 37 CFR 1.85**

New corrected drawings must be filed with the changes incorporated therein. Identifying indicia, if provided, should include the title of the invention, inventor's name, and application number, or docket number (if any) if an application number has not been assigned to the application. If this information is provided, it must be placed on the front of each sheet and centered within the top margin. If corrected drawings are required in a Notice of Allowability (PTOL-37), the new drawings **MUST** be filed within the **THREE MONTH** shortened statutory period set for reply in the "Notice of Allowability."

Extensions of time may NOT be obtained under the provisions of 37 CFR 1.136 for filing the corrected drawings after the mailing of a Notice of Allowability. The drawings should be filed as a separate paper with a transmittal letter addressed to the Official Draftsperson.

### 6. **Corrections other than Informalities Noted by Draftsperson on form PTO-948.**

All changes to the drawings, other than informalities noted by the Draftsperson, **MUST** be made in the same manner as above except that, normally, a highlighted (preferably red ink) sketch of the changes to be incorporated into the new drawings **MUST** be approved by the examiner before the application will be allowed. No changes will be permitted to be made, other than correction of informalities, unless the examiner has approved the proposed changes.

### **Timing of Corrections**

Applicant is required to submit acceptable corrected drawings within the time period set in the Office action. See 37 CFR 1.185(a). Failure to take corrective action within the set (or extended) period will result in **ABANDONMENT** of the application.

## *Claim Rejections - 35 USC § 102*

7. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.



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8. Claims 1-9 and 11 are rejected under 35 U.S.C. 102(a) as being anticipated by the current application's own admitted prior art.

Regarding claim 1, the background of the current invention discloses a solid-state imaging device comprising: a pixel unit [Fig. 7, 1] constituted by a two-dimensional array of pixels for generating charge in correspondence to received light and accumulating the charge for a predetermined period of time; a vertical transfer unit [Fig. 7, 2] for vertically transferring charge from the pixels in the pixel unit; a horizontal transfer unit for horizontally transferring charge from the vertical transfer unit; shift gates [Fig. 7, 3] each provided between each pixel and the vertical transfer unit for reading out the charge in the pixels to the vertical transfer unit, gate electrodes [Fig. 7, 4A] for controlling the shift gates; and a plurality of lead lines [Fig. 7, 4B] and a plurality of connection terminals [Fig. 7, 6] for connecting the gate electrodes to an external circuit [Fig. 7, 5]; the gate electrodes making up N [wherein  $N = 17$ ] of gate electrode groups in which the lines belonging to each coset of modulo 17 within successive pixel rows are connected to common lead lines, 17 being a predetermined natural number between 4 and one half the number of pixels [with the number of pixels  $\geq 32$ ] in a column, and also being a minimum number corresponding to a periodic unit of gate electrode connections to the connection terminals within the successive pixel rows, the gate electrodes having common connection terminals to reduce the number (i.e. 16) of the connection terminals to less than 17 (see Page 2, Line 15 - Page 5, Line 6).

Regarding claim 2, this claim is rejected under the reasoning applied in the above rejection of claim 1; furthermore, the background of the current invention discloses gate



electrodes/gate control lines [Fig. 7, 4A] connected to gate electrode groups in which horizontal lines belonging to each coset of modulo 17 [wherein  $N=17$ ] within successive pixel rows are connected commonly, being combined with each other so as to reduce the number (i.e. 16) of the connection terminals to less than 17 (see Page 2, Line 15 - Page 5, Line 6).

Regarding claim 3, this claim is rejected under the reasoning applied in the above rejection of claim 1; furthermore, the background of the current invention discloses the gate electrodes being provided in a predetermined number 17 [wherein  $N = 17$ ] of gate electrode groups such that the horizontal line number of the gate electrode groups which are connected to respective common lead lines belong to each same residue class of modulo 17, 17 being a predetermined natural number between 4 and one half the number of pixels [with the number of pixels  $\geq 32$ ] in a column, and also being a minimum number corresponding to the periodic unit about connections to the connection terminals within the successive pixel rows, some of the gate electrode groups being commonly connected so that the connection electrodes are less in number (i.e. 16) than 17 (see Page 2, Line 15 - Page 5, Line 6).

Regarding claim 4, this claim is rejected under the reasoning applied in the above rejection of claim 1; furthermore, the background of the current invention discloses the commonly connected gate electrode groups are always controlled in the same way in each of all predetermined read-out modes including selective pixel read-out modes by selective shift gate driving (see Fig. 7; Page 4, Line 8 - Page 5, Line 6).



Regarding claim 5, this claim is rejected under the reasoning applied in the above rejection of claims 1, 2 and 4.

Regarding claim 6, this claim is rejected under the reasoning applied in the above rejection of claims 1, 3 and 4.

Regarding claims 7-9, the background of the current invention discloses gate electrode groups controlled in each of all the predetermined read-out modes are set such as to provide a minimum number of connection terminals for connecting the gate electrodes to an external circuit (see Fig. 7; Page 4, Line 8 - Page 5, Line 6; wherein 16 connection terminals is the minimum for this particular display device).

Regarding claim 11, this claim is rejected under the reasoning applied in the above rejection of claim 1; furthermore, the background of the current invention discloses a plurality of  $R$  (i.e. 34) gate electrodes, wherein the  $R$  (i.e. 34) gate electrodes are divided into  $S$  (i.e. 17) gate groups, each gate group having  $R/S$  (i.e.  $34/17 = 2$ ) gate electrodes, wherein the  $R$  (i.e. 34) gate electrodes are divided into  $R/S$  (i.e.  $34/17 = 2$ ) pixel groups, each having  $R/(R/S)$  (i.e.  $34/2 = 17$ ) consecutive, adjacent, gate electrodes, wherein the  $i$ th gate electrode of each pixel group, where  $i = 1$  to  $R/(R/S)$  (i.e.  $34/2 = 17$ ), share a common connection terminal, and wherein at least two gate electrodes within a given pixel group (e.g. 16a) share a common connection terminal (see Fig. 4; Page 4, Line 8 - Page 5, Line 21).



*Response to Arguments*

9. Applicants' arguments filed October 15, 2002 have been fully considered but they are not persuasive. The applicants contend that the current application's own admitted prior art does not teach a number N of gate electrode groups, where N is the minimum number corresponding to a periodic unit about connections to connection terminals within successive pixel rows, and connection terminals being combined to reduce the number of connection terminals to less than N. The examiner respectfully disagrees. Figure 6 illustrates a number N (i.e. 16) of gate electrode [Fig. 6, 3] groups, where N (i.e. 16) is the minimum number corresponding to a periodic unit about connections to connection terminals within successive pixel [Fig. 6, 1] rows, and connection terminals [Fig. 6, 5] being combined to reduce the number of connection terminals (i.e. 4) to less than N (i.e. 16) (see page 2, line 15 - page 4, line 7). Furthermore, Figure 7 illustrates a number N (i.e. 17) of gate electrode [Fig. 7, 3] groups, where N (i.e. 17) is the minimum number corresponding to a periodic unit about connections to connection terminals within successive pixel [Fig. 7, 1] rows, and connection terminals [Fig. 7, 5] being combined to reduce the number of connection terminals (i.e. 16) to less than N (i.e. 17) (see page 4, line 8 - page 5, line 21). By such reasoning, rejection of the claims is deemed proper and thereby maintained.



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*Conclusion*

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jeff Piziali whose telephone number is (703) 305-8382. The examiner can normally be reached on Monday - Friday (6:30AM - 3PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala can be reached on (703) 305-4938. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9314 for regular communications and (703) 872-9314 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-4700.

  
J.P.

March 6, 2003



BIPIN SHALWALA  
PATENT EXAMINER  
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